1/10

FIG. 1 (PRIOR ART)

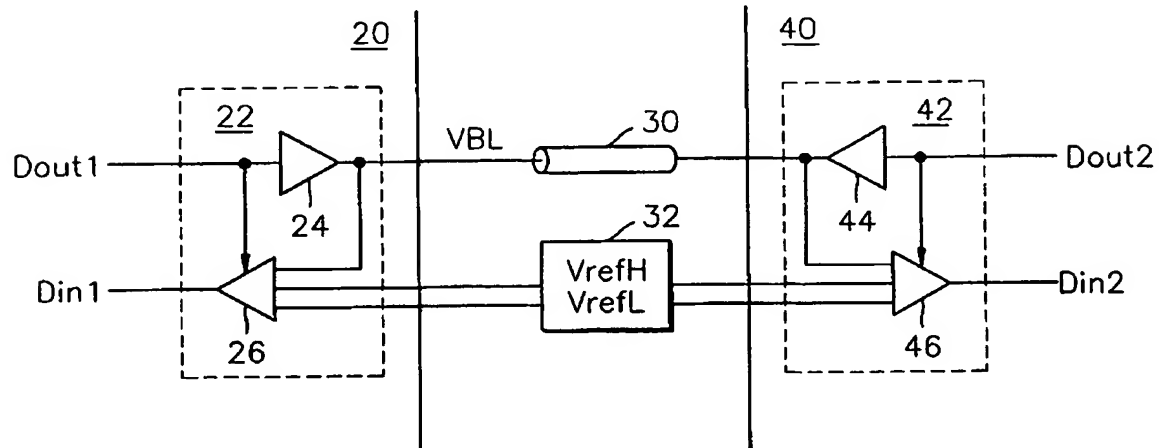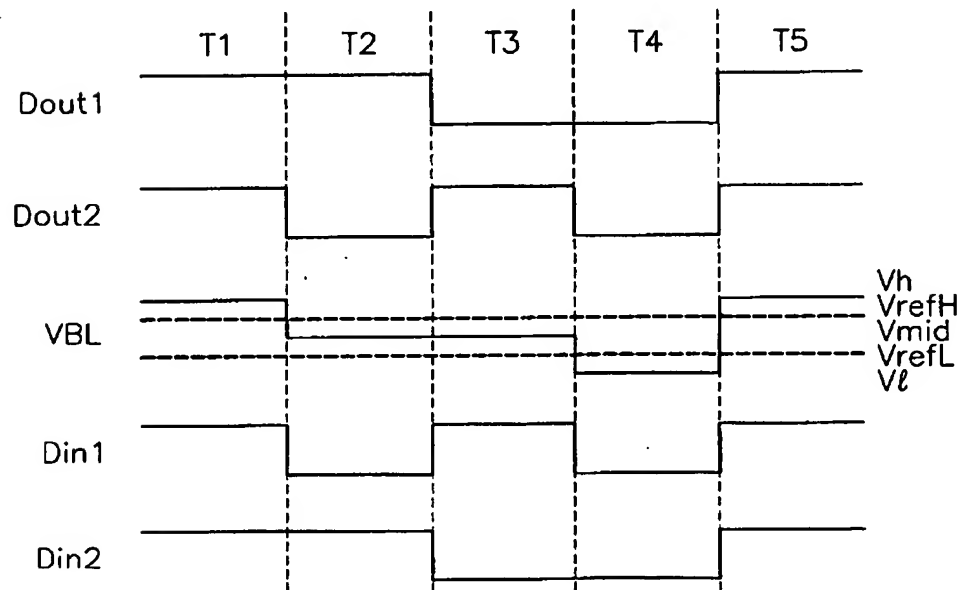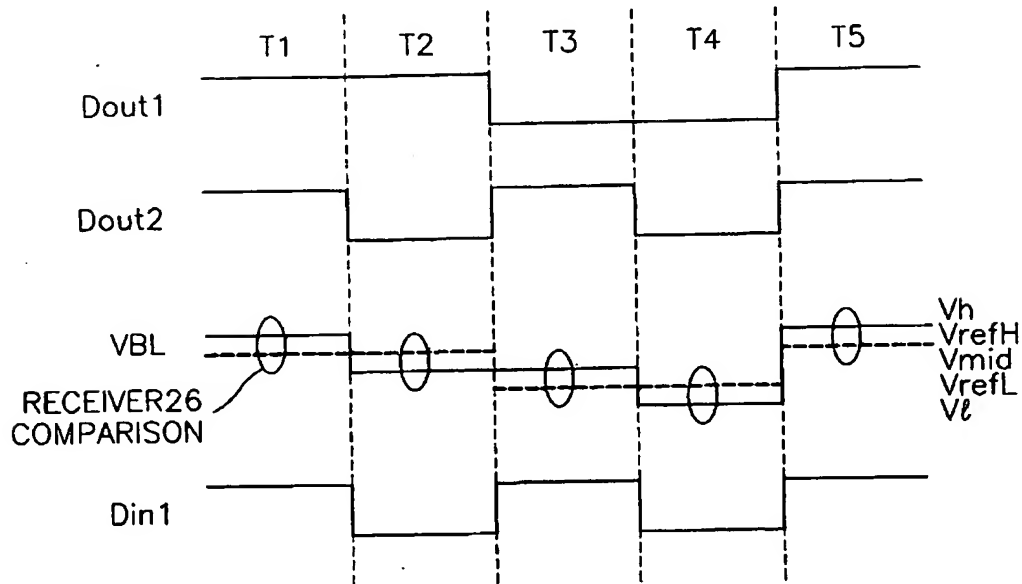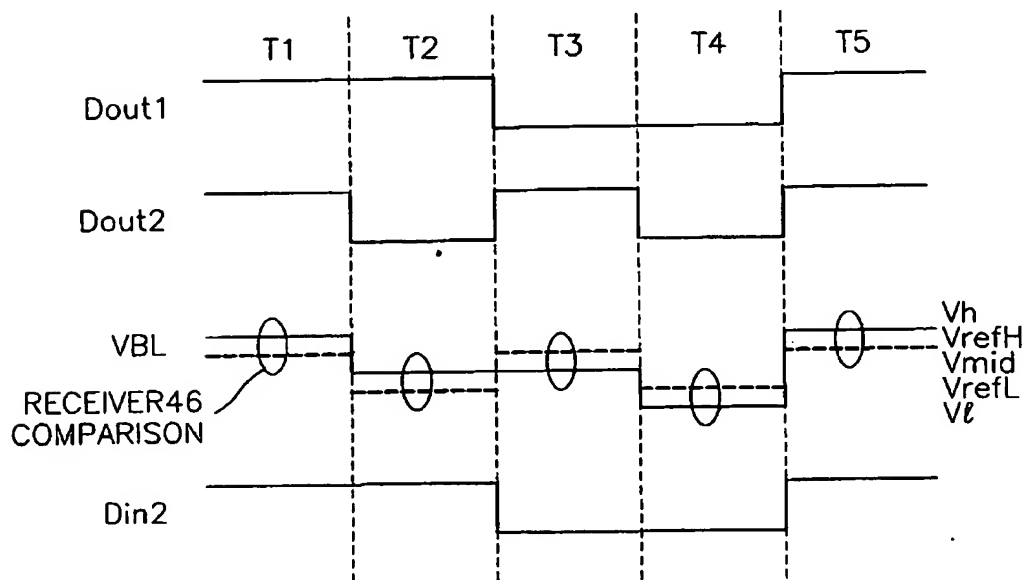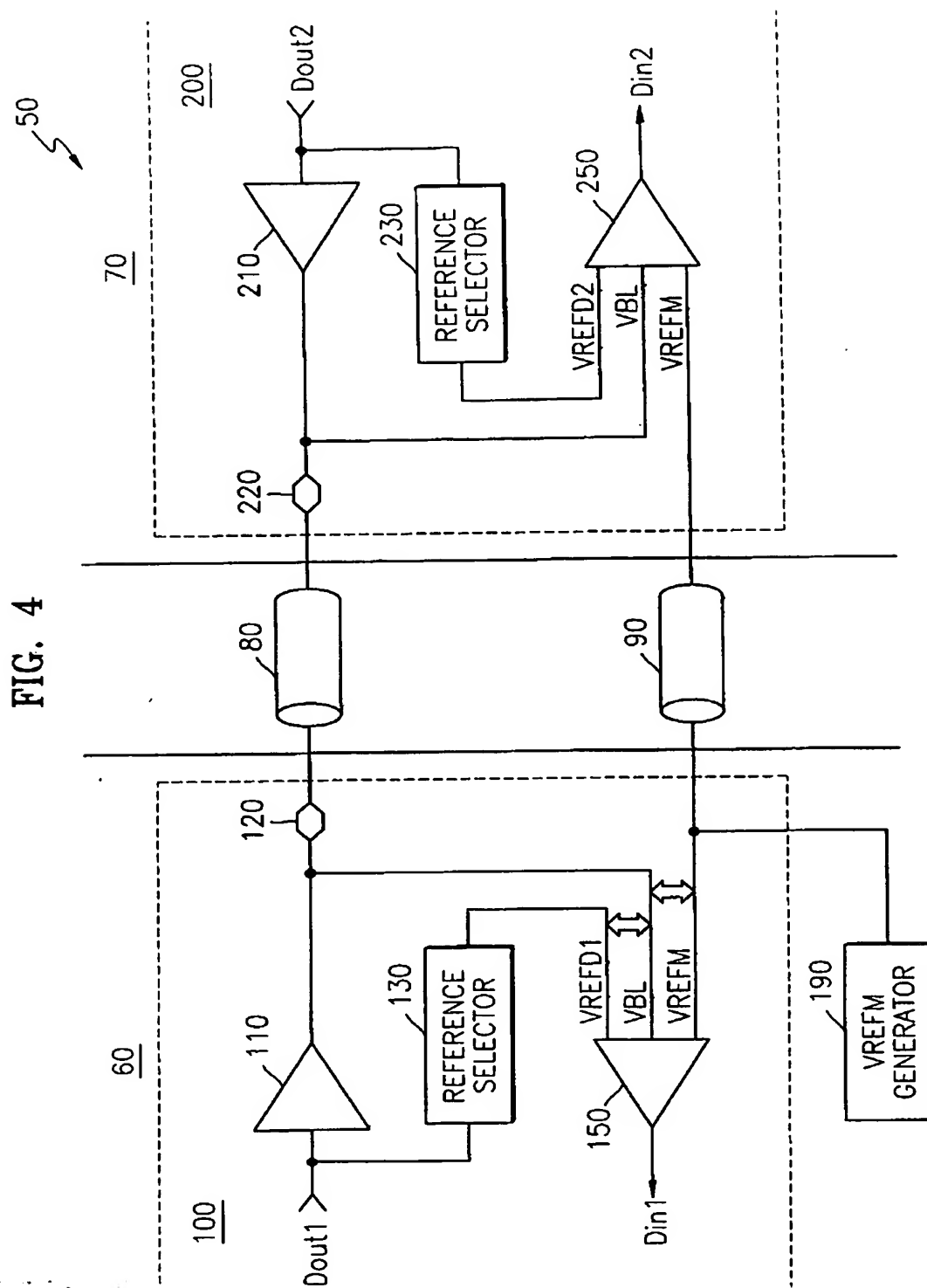# FIG. 2 (PRIOR ART)

2/10

FIG. 3A (PRIOR ART)

FIG. 3B (PRIOR ART)

Woo-Seop KIM
SIMULTANEOUS BIDIRECTIONAL INPUT/OUTPUT CIRCUIT AND METHOD
Attorney Docket No. 9898-317/Application No. 10/748,748

3/10

FIG. 4

4/10

FIG. 5A

FIG. 5B

5/10

FIG. 6

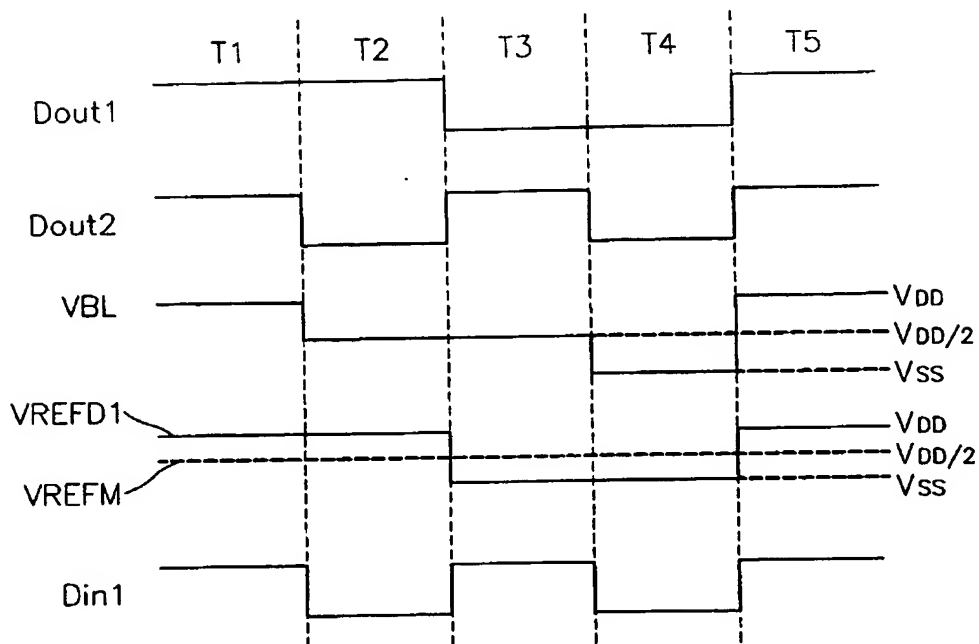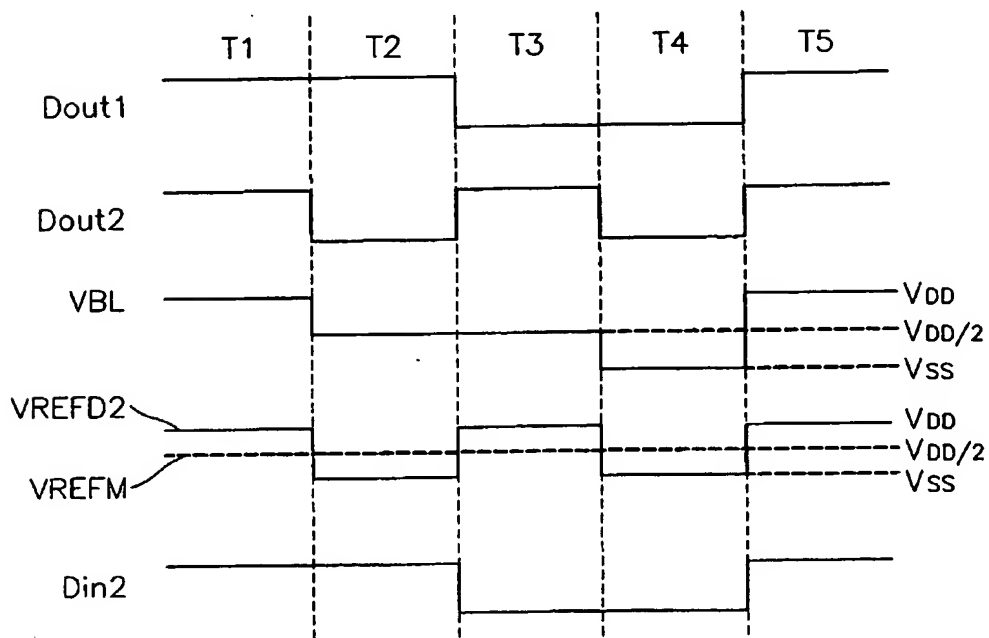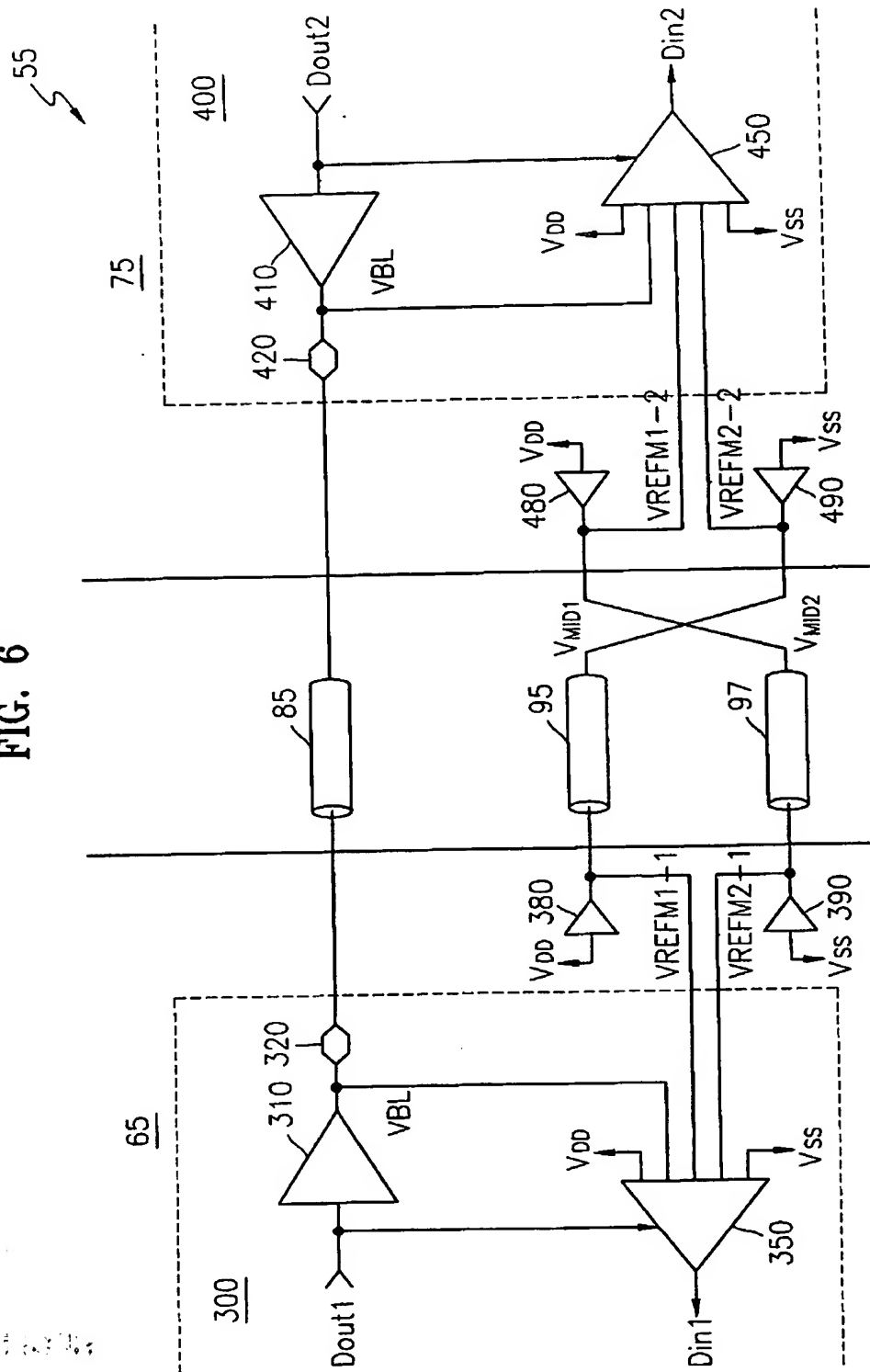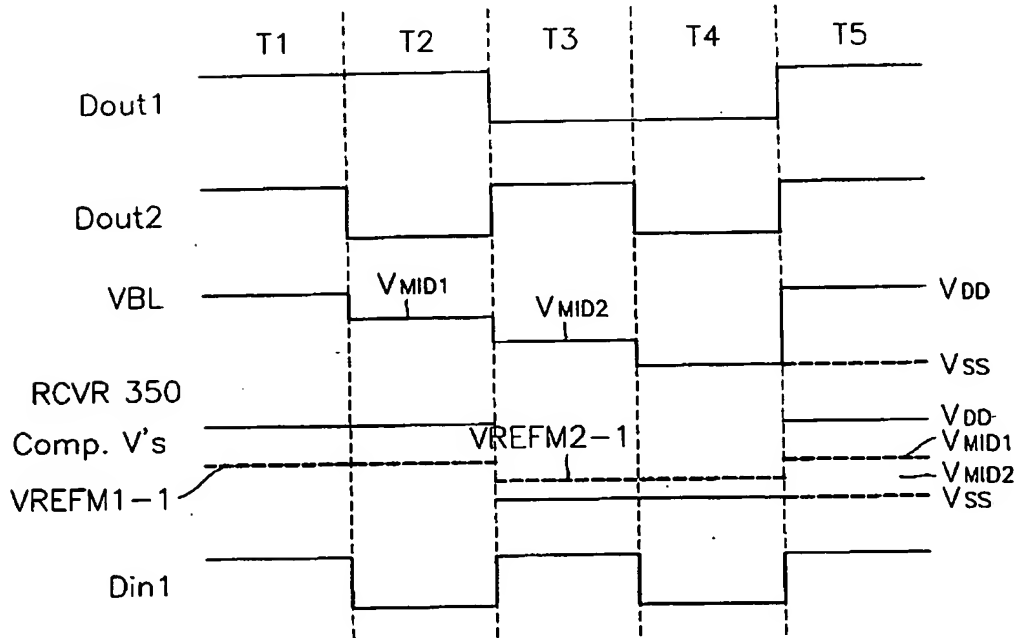6/10

FIG. 7A

FIG. 7B

7/10

FIG. 8

Woo-Seop KIM
SIMULTANEOUS BIDIRECTIONAL INPUT/OUTPUT CIRCUIT AND METHOD
Attorney Docket No. 9898-317/Application No. 10/748,748

8/10

FIG. 9

130

VL

P7

Dout   VREFD1

N7

VH

FIG. 10

130

VL

T1

I 1   Dout1#   VREFD1

VH   T2

Dout1

9/10

FIG. 11
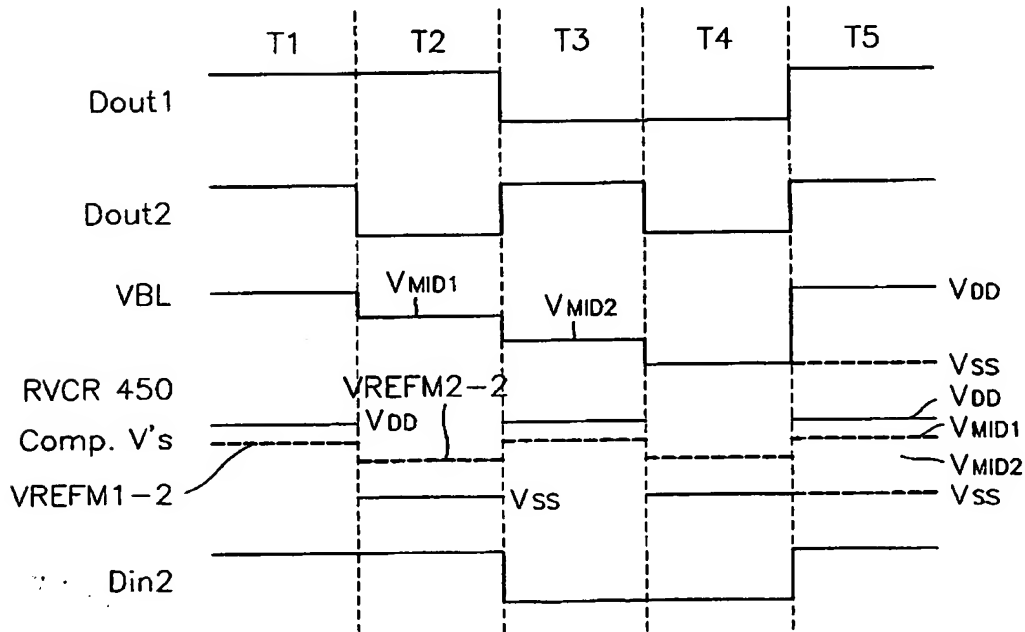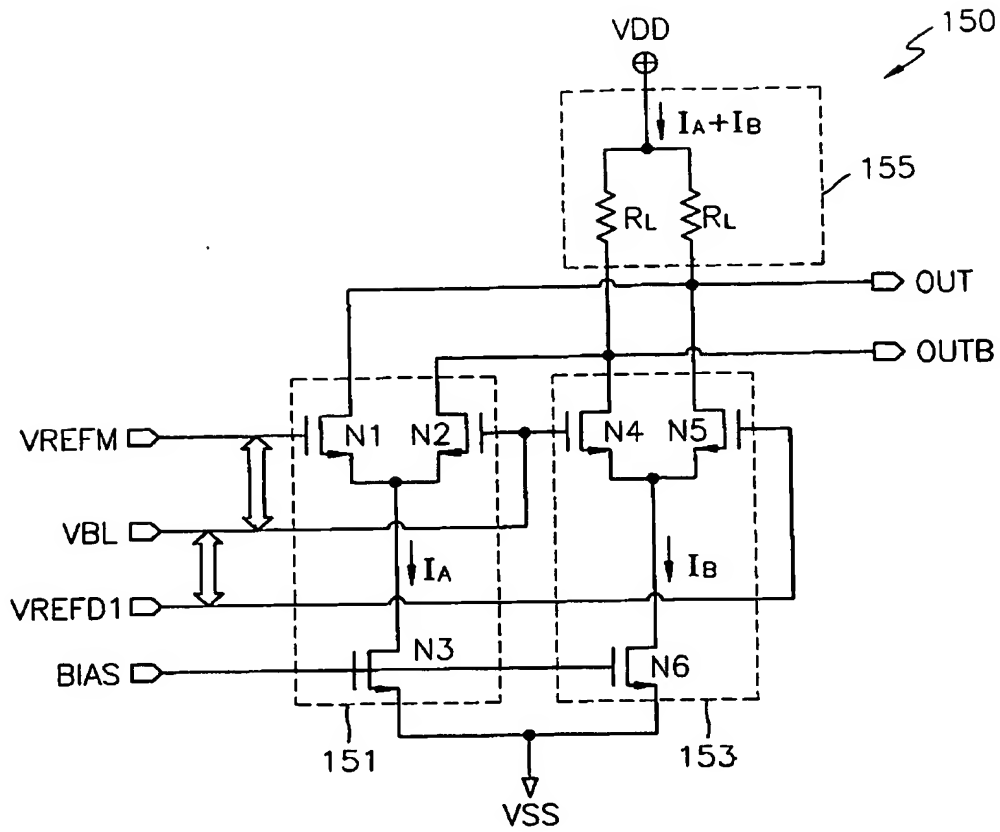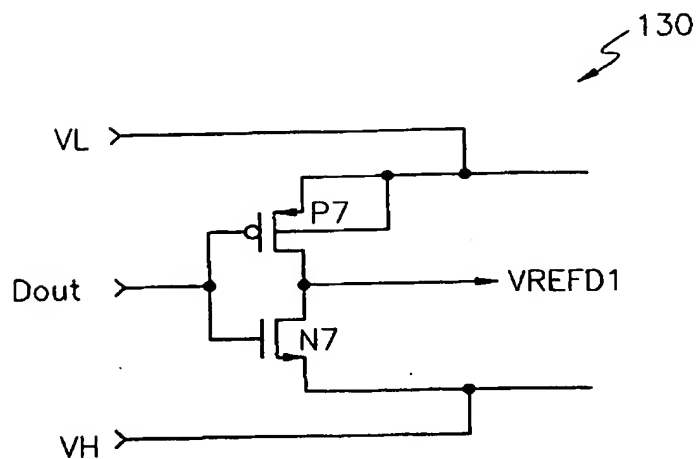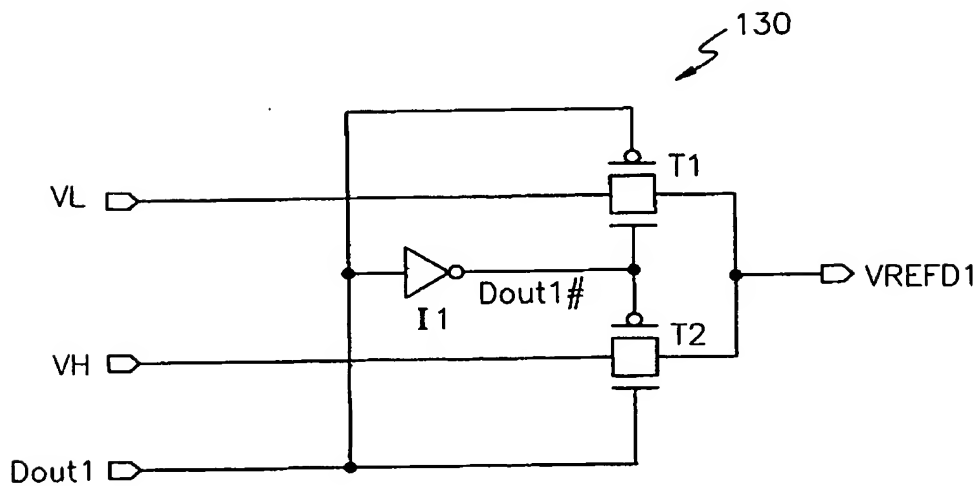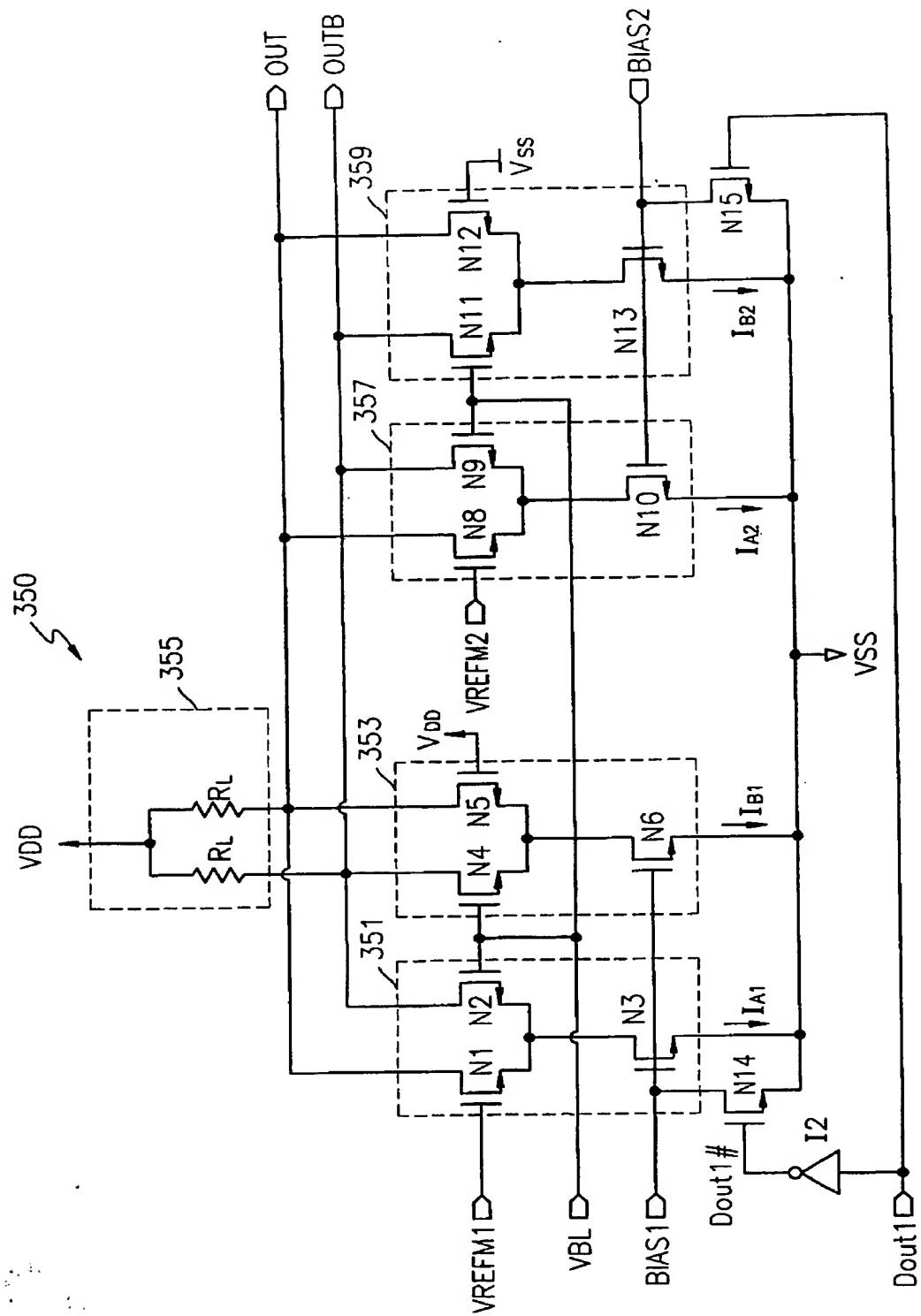
Woo-Seop KIM
SIMULTANEOUS BIDIRECTIONAL INPUT/OUTPUT CIRCUIT AND METHOD
Attorney Docket No. 9898-317/Application No. 10/748,748

10/10

FIG. 12